

P572 Firmware

Release Notes

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4v5r2 – 26-Jan-2023

Changes:

- Change LED3 in RMU mode to turn on when connected and occlude on modbus packet receipt
 - Diagnostic output colour change to red for on (alarm amber when active)
 - Diagnostic Analog output now shows "Not Fitted" if digital only variant (was "Error")
 - Newflow branding label RTU2 changed to RMU
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4v5r1 – 7-Apr-2022

Changes:

- The diagnostics counts now shown as 32 bit values following a customer query
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4v5r0 – 28-Feb-2022

Changes:

- Enterprise request, CDM100 Densitometer interface option added
 - Analog processing refined (dropped occasional updates in PIU mode)
 - Short/Long Proof Type options added for 10MHz or 625KHz FPGA subsampling
 - Short Proof (7mins max), Long Proof (114mins max)
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4v4r0 – 21-Feb-2022

Changes:

- Live CountB Modbus register fix in dual chronometry block fixed
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4v3r0 – 18-Feb-2022

Changes:

- Modbus map extended to return combined Dual Chronometry Data and digital/analog data to support PC proving control
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4v2r0 – Not Released

Changes:

- Prover timeout S1-S2 extended to 300secs (from 60)
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4v1r1 – 28-Feb-2022

Changes:

- Encoder position not updated on diags page without a refresh
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4v1r0 – 25-Nov-2021

Changes:

- Diagnostic page major rework for speed boost (10Hz)
 - Newflow/AMR branding added in P572drv.h (RTU2/RMU)
 - Fix misreporting on 4v0r2 of inverted inputs on diagnostics (OFF-HI an ON-LO interchanged)
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4v0r2 – 22-Nov-2021

Changes:

- HSDIGIO fixed (outputs were being pulsed)
 - Inputs not being read at full speed
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4v0r1 – 4-Nov-2021

Changes:

- Added a password fail message menu if the incorrect password is supplied
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4v0r0 – 28-Oct-2021

Changes:

- The altera now gives out pulse count and time snap as well as the time and counts for all 5 input channels (A&B as before + C&D and Rawin).
 - This Version 4 firmware is now able to replicate exactly the CIU functionality. Note You must use IO Firmare 2v04_2v09 minimum on Gen 2 firmware.
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Versions prior to 4v0r0 not published